



Chandler Unified School District

EGR305A Honors Digital Electronics
SY 2023-24



Course Overview

Course Description

Digital Electronics Technology is a course of study in applied digital logic that encompasses the design and application of electronic circuits and devices found in video games, watches, calculators, digital cameras, and thousands of other devices. Instruction includes the application of engineering and scientific principles as well as the use of Boolean algebra to solve design problems. Using computer software that reflects current industry standards, activities provide opportunities for students to design, construct, test, and analyze simple and complex digital circuitry.

AP/IB/Dual Enrollment

This not an AP, IB, or Dual Enrollment course.

Prerequisite/Fee(s)

\$20

Course Materials

- Engineering Notebook
- Pencils and Pens
- Paper
- Colored Pencils

Course Attire: Students are responsible for the following

None for this course

Adopted Resource(s)

Teacher Provided

**An asterisk will indicate a resource containing sexually explicit materials per legislative definitions. CUSD has determined that all resources listed above are of exceptional educational value.*

Site and Faculty Information

School name and address:

Camille Casteel High School, 24901 S Power Rd, Queen Creek, AZ 85142

Building principal:

Jayson Phillips
phillips.jayson@cusd80.com

Teacher:

Paul Keith BS & MEd
keith.paul@cusd80.com

Office hours: Thursday 2:30 to 3:00 pm or upon request

Course Access

This course is taught in-person at Camille Casteel High School. Students will have access to the curriculum and instruction in the classroom. Google Classroom is the primary location for students to access material when absent from classes taught in person.

Equipment Usage Agreement:

Students will receive training for the proper use and care of all equipment. I understand that while my student uses school equipment, they are responsible for its care. If school equipment is damaged, lost, stolen, or destroyed under my student's care, I further understand that my student and I will be responsible for the replacement or cost of repair of the damaged equipment.

CTE Program Responsibilities:

Career & Technical Student Organization (CTSO)

Career & Technical Student Organizations (CTSOs) are co-curricular organizations that help enrich applied academics, communications, and teamwork. Students in CTE programs can participate in CTSO leadership activities and competitive events.

This program is aligned with the following CTSO: SkillsUSA.

Industry Credentials

CTE programs allow each student to earn an industry certification aligned to their CTE program area. This CTE program will offer the following industry credential(s) for all students by program completion:

Industry credentials do not apply to this course

Technical Skills Assessment (TSA)

Technical Skills Assessments (TSA) are an Arizona Department of Education high-stakes state assessment designed for each program area. The TSA is to certify and document student attainment of industry-validated knowledge and skills through online testing. The TSA is taken after the CTE program (year 2 or 3) of each program.

Non-Discrimination Clause/ Aviso de no discriminación

The non-discrimination clause can be accessed using the link or QR code (www.cusd80.com/Page/983)



Help

Academic Support

- Contact the teacher to schedule an appointment during office hours
- [Ed Tech](#) support for students, parents/guardians, and community link (cusd80.com/Page/45109)

Mental Health Support

- CUSD mental health support cusd80.com/Domain/10528 or 480-573-8808 (talk or text)
- Suicide & Crisis Lifeline: 9-8-8 hotline
- 24-hour Crisis Line Talk: 602-222-9444, Text: 741-741

Student Conduct, Success, and Responsibilities

Student Handbook

Students must follow the policies and procedures established in the Student Handbook. Copies of the handbook can be found at cusd80.com/handbooks. Printed copies will be provided upon request.

Student Responsibilities

Students are responsible to complete classwork and projects in a timely manner. Students will be assessed a late penalty if they are not using their class time productively. Students are expected to demonstrate honesty and integrity while in attendance at Casteel High School. We expect each student to do his or her own work. This includes taking tests, completing homework, and other class assignments. If submitted work is not a true reflection of a student's own effort and ability, then the student has manifested unacceptable academic behavior and

the assignment will receive a zero.

Late work

All classwork and homework is required to be completed and turned in on time. Late homework will be marked off, 25% off for 1 day late and 50% for 2 days late. Assignments over 1 week late will receive a 0 in the gradebook (teacher discretion). If a student has an excused absence, it is his or her responsibility to make up any work that was missed, including labs and assignments.

Assessments and Assignments

Students will complete assessments during each unit of study to assess their understanding. Students will complete the CUSD Common Final at the end of the first and second semesters. The Common Final will count for 20% of the student's final semester grade in grades 9-12 and 10% in grades 7-8 (some exceptions may apply at the Junior High level). Common finals will be in ELA, Math, Science, Social Science, and World Language.

Final exams will be given during the CUSD Jr High/High School Early Dismissal days in December and May, as identified on the [District Calendar](#).

If students are requested to participate in a survey, the survey questions will be provided to parents/guardians seven days before student contact.

Grading

Grade Percentage

A	B	C	D	F
90% - 100%	80% - 89%	70% - 79%	60% - 69%	<60%

Quarter grades

Grades consist of a combination of quizzes/tests, lab reports, projects, notebook checks, and in class work.

Semester grades

Semester grades are calculated using 40/40/20: Each quarter accounts for 40% of the semester grade and the final exam accounts for the remaining 20%.

Units of study

Units for EGR305A Honors Digital Electronics

- Foundation in Electronics
- Combinational Logic
- Sequential Logic
- Controlling Real World Systems

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Parents/Guardians should indicate if they "Acknowledge" or have a "Potential Conflict" with their student's participation in the following units by checking the appropriate box for each unit of study listed. Students cannot opt out of a standards-based unit. Marking "Potential Conflict" will prompt the teacher to make contact regarding assignment alternatives.

Unit of study	Acknowledge	Potential Conflict
-Foundation in Electronics		
-Combinational Logic	<input type="checkbox"/>	<input type="checkbox"/>
-Sequential Logic	<input type="checkbox"/>	<input type="checkbox"/>
-Controlling Real World Systems	<input type="checkbox"/>	<input type="checkbox"/>

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Parent/Guardian

Acknowledgment

By signing and returning this form, the parent/guardian acknowledges they have reviewed the resources and units of the study included in the syllabus.

- As the parent/guardian, I understand that I may contact the teacher if I have questions about the resources, content, or units of study.
- As the parent/guardian, I understand I can check my student's grades in Infinite Campus anytime during the school year.

Student name (printed)

Student signature

Parent/Guardian name (printed)

Parent Signature

Date

Please return this page to your student's teacher.